Birzeit University logo

Faculty of Engineering and Technology

Electrical and Computer Engineering Department

Advanced Digital Design ENCS3310

Homework#1

For the following circuit with 4 inputs (S1,S0,A,B) and two outputs : S and Cout:

1. Write a Verilog description for the FA, Comp, MUX, and then build the circuit structurally.
2. Build the same circuit behaviorally.
3. Write a test bench for the circuit that can test all possible cases of the inputs.

